

**CHI MEI**
OPTOELECTRONICS CORP.**DOC No. 24044110**

Issued Date: May. 19, 2004

Model No.: V270W1 - L03

Approval

TFT LCD Approval Specification

MODEL NO.: V270W1 - L03

Customer: _____

Approved by: _____

Note:

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**REVISION HISTORY**

Version	Date	Page (New)	Section	Description
Ver 1.0	August 1,03	All	All	Preliminary Specification is first issued.
Ver 2.0	Sep. 18,03	17	7.2	Contrast ratio:Typ. (600)→ 600 Response time TR:Typ. (15)→ 15 TF: Typ. (10)→ 10 Gray to Gray: Typ (16.6)→ 16.6 Center Luminance of White: Min. (450)→ 450 Typ. (550)→ 550 Average Luminance of White: Min. (400)→ 400 Typ. (450)→ 450 Color Chromaticity Min . Typ. Max. Min . Typ. Max. Red Rx (0.616)(0.646)(0.676)→ 0.616 0.646 0.676 Ry (0.302)(0.332)(0.362)→ 0.302 0.332 0.362 Green Gx (0.239)(0.269)(0.299)→ 0.239 0.269 0.299 Gy (0.570)(0.600)(0.630)→ 0.570 0.600 0.630 Blue Bx (0.112)(0.142)(0.172)→ 0.112 0.142 0.172 By (0.042)(0.072)(0.102)→ 0.042 0.072 0.102 Viewing Angle Horizontal θ x+ Typ. (85)→ 85 θ x- Typ. (85)→ 85 Vertical θ Y+ Typ. (85)→ 85 θ Y- Typ. (85)→ 85
		5	2.1	Shock (Non-Operating) Max. Value (100)→ 100
Ver.2.1	Oct.16, 03	17	7.2	Vibration (Non-Operating) Max. Value (1.0)→ 1.0 Viewing Angle Horizontal θ x+ Min.. 80 θ x- Min. 80 Vertical θ Y+ Min. 80 θ Y- Min. 80
Ver.2.2	Nov. 24,03	25	11	Mechanical drawing is updated.
	Jan.07,04	23	9.1	Serial ID includes the information as below: (a) Manufactured Date: Year: 1~9→ 0~9
	Jan.19,04	25	11	Mechanical drawing is updated.
	May 19,04	6	2.2.2	Update BACKLIGHT UNIT table Add Note (3), (4)
		8	3.2.1	Add 3.2.1 CCFL CHARACTERISTICS
			3.2.2	Add 3.2.2 INVERTER CHARACTERISTICS
				Update Note (2), (4), (5), (6)
		10	3.2.3	Add 3.2.3 INVERTER INTERFACE CHARACTERISTICS Add Note (1), (2)
		14	5.2	Update 5.2 BACKLIGHT UNIT
		15	5.3	Add 5.3 INVERTER UNIT
		21	7.1	Lamp Current Value 4.7mA→4.7±0.3 mA Inverter Driving Frequency →Oscillating Frequency (Inverter) Value 56 KHz→ 56±2KHz

1. GENERAL DESCRIPTION

1.1 OVERVIEW

V270W1- L03 is a 27" TFT Liquid Crystal Display module with 14-CCFL Backlight unit and 1ch-LVDS interface. This module supports 1280 x 720 WXGA format and can display true 16.7M colors (8-bit/color). The inverter module for backlight is build-in.

1.2 FEATURES

- Ultra wide viewing angle – Super MVA technology
- High brightness (550 nits)
- High contrast ratio (600:1)
- Fast response time
- High color saturation NTSC 75%
- WXGA (1280 x 720 pixels) resolution, true HDTV format.
- DE (Data Enable) only mode
- LVDS (Low Voltage Differential Signaling) interface

1.3 APPLICATION

- TFT LCD TVs

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	597.12(H) x 335.88 (V) (26.97" diagonal)	mm	(1)
Bezel Opening Area	603.22 (H) x 341.98 (V)	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1280 x R.G.B. x 720	pixel	-
Pixel Pitch (Sub Pixel)	0.1555 (H) x 0.4665 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.7M	color	-
Display Operation Mode	Transmissive mode / Normally black	-	-
Surface Treatment	Anti-glare with anti-reflective coating Hard coating (2H), Haze: 40% Reflection Rate: < 2%	-	-

1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)		637.55		mm	Module Size Depth(D)
	Vertical(V)		379.8		mm	
	Depth(D) W/O INV	-		36	mm	
	Depth(D) W/I INV	40	40.5	41	mm	
Weight		-	4300		g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2) Module Depth does not include connectors.



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2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T _{ST}	-20	+60	°C	(1)
Operating Ambient Temperature	T _{OP}	0	+50	°C	(1), (2)
Shock (Non-Operating)	S _{NOP}	-	100	G	(3), (5)
Vibration (Non-Operating)	V _{NOP}	-	1.0	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. ($T_a \leq 40$ °C).

(b) Wet-bulb temperature should be 39 °C Max. ($T_a > 40$ °C).

(c) No condensation.

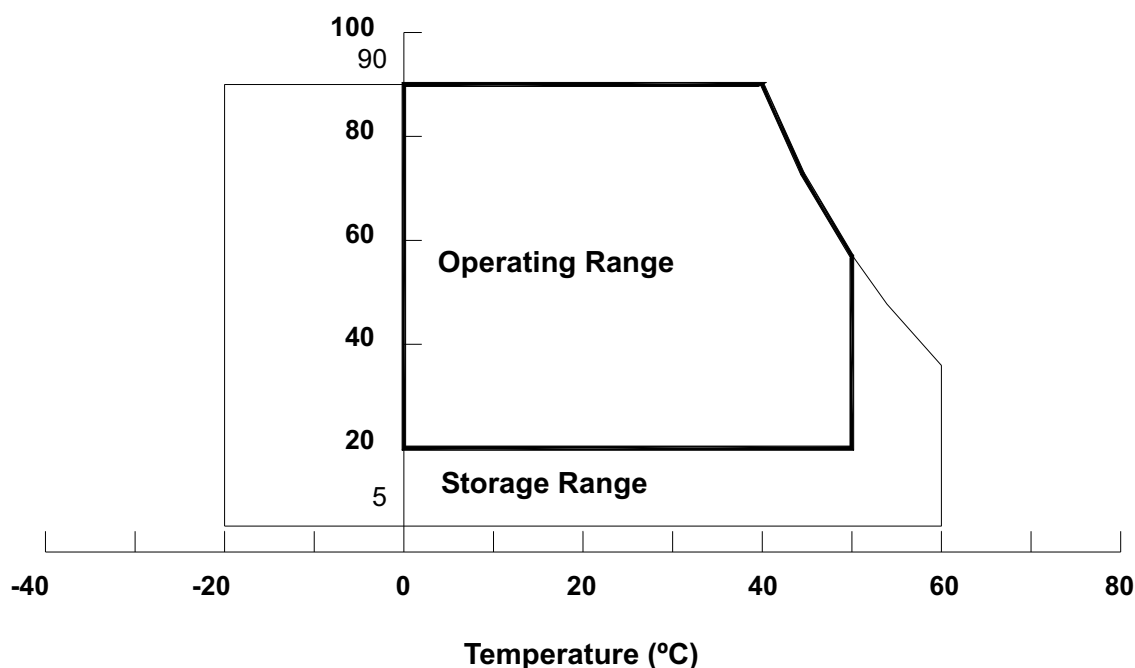
Note (2) The temperature of panel display area surface should be 0 °C Min. and 60 °C Max.

Note (3) 2 ms, half sine wave, 1 time for $\pm X$, $\pm Y$, $\pm Z$.

Note (4) 10 ~ 500 Hz, 10 min, 1 time each X, Y, Z.

Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

Relative Humidity (%RH)



2.2 ELECTRICAL ABSOLUTE RATINGS**2.2.1 TFT LCD MODULE**

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	V _{CC}	-0.3	+6.0	V	(1)
Logic Input Voltage	V _{IN}	-0.3	4.3	V	

2.2.2 BACKLIGHT UNIT

Item	Symbol	Test Condition	Min.	Type	Max.	Unit	Note
Lamp Voltage	V _W	Ta = 25 °C	—	—	3000	V _{RMS}	
Input Voltage	V _{BL}	—	0	—	(26.4)	V	(1), (2)
Control Signal Level	—	—	-0.3	—	7	V	(1), (2), (4)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Functional operation should be restricted to the conditions described under normal operating conditions.

Note (2) Specified values are for lamp and inverter (Refer to 3.2 for further information).

Note (3) Protect inverters from moisture condensation and freezing.

Note (4) The control signals includes On/Off Control, Internal PWM Control, External PWM Control and Internal/External PWM Selection.

3. ELECTRICAL CHARACTERISTICS**3.1 TFT LCD MODULE**

Ta = 25 ± 2 °C

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V _{CC}	4.5	5.0	5.5	V	-
Ripple Voltage		V _{RP}	-	-	200	mV	-
Rush Current		I _{RUSH}	-	2.1	3	A	(2)
Power Supply Current	White	I _{CC}	-	1.4	-	A	(3)a
	Black		-	1	-	A	(3)b
	Vertical Stripe		-	1.2	-	A	(3)c
LVDS differential input high threshold voltage		V _{TH}	-	-	+100	mV	
LVDS differential input low threshold voltage		V _{TL}	-100	-	-	mV	
LVDS common input voltage		V _{IC}	1.125	1.25	1.375	V	
Terminating Resistor		R _T	-	100	-	ohm	

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



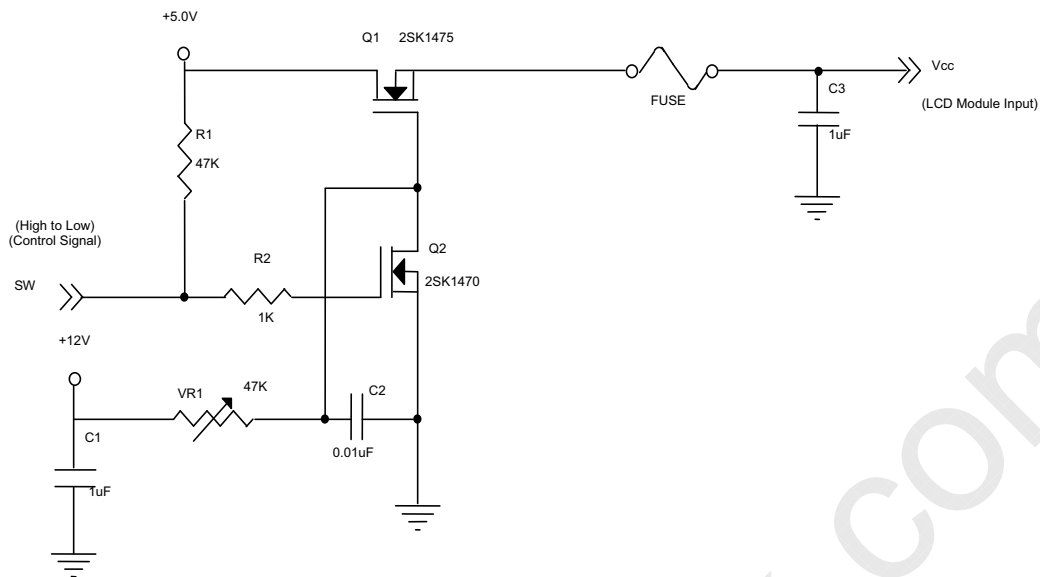
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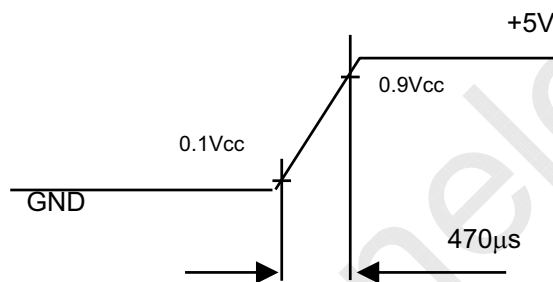
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Vcc rising time is 470μs



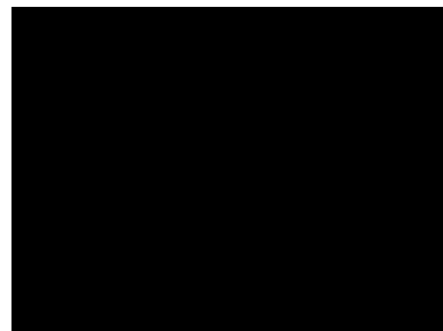
Note (3) The specified power supply current is under the conditions at $V_{cc} = 5\text{ V}$, $T_a = 25 \pm 2\text{ }^{\circ}\text{C}$, $f_v = 60\text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



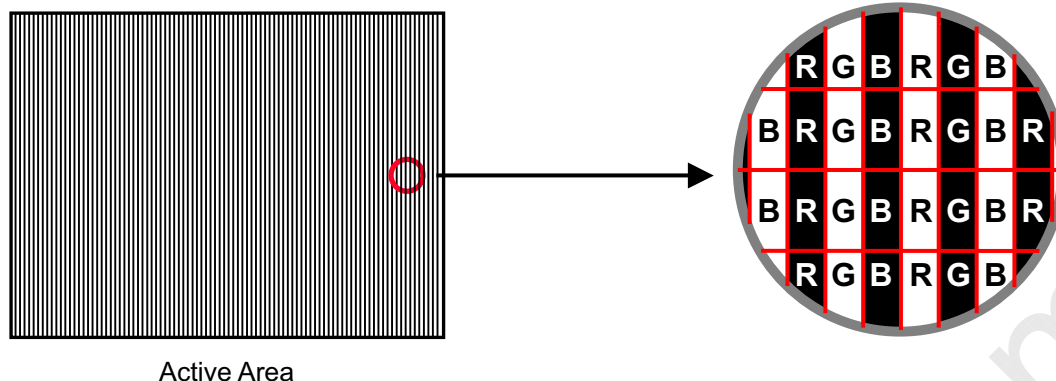
Active Area

b. Black Pattern



Active Area

c. Vertical Stripe Pattern

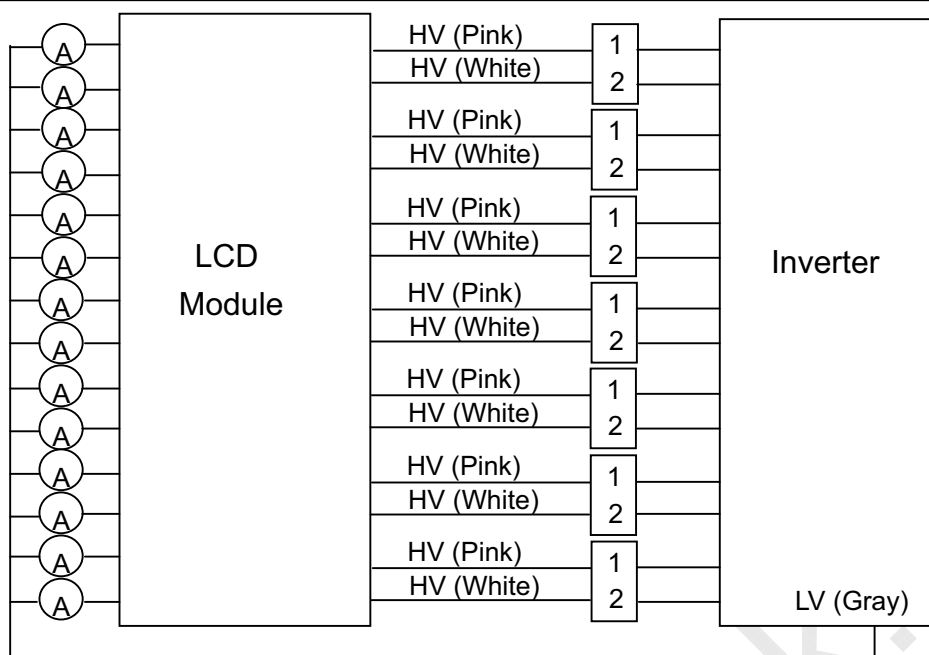
**3.2 BACKLIGHT UNIT****3.2.1 CCFL (Cold Cathode Fluorescent Lamp) CHARACTERISTICS** ($T_a = 25 \pm 2^\circ\text{C}$)

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lamp Voltage	V_W	-	1120	-	V_{RMS}	$I_L = 4.7\text{mA}$
Lamp Current	I_L	4.4	4.7	5.0	mA_{RMS}	(1)
Lamp Starting Voltage	V_S	-	1790	-	V_{RMS}	(2), $T_a = 0^\circ\text{C}$
		-	1200	-	V_{RMS}	(2), $T_a = 25^\circ\text{C}$
Operating Frequency	F_O	50	-	70	KHz	(3)
Lamp Life Time	L_{BL}	50K	60K	-	Hrs	(4)

3.2.2 INVERTER CHARACTERISTICS ($T_a = 25 \pm 2^\circ\text{C}$)

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Consumption	P_{BL}	-	92	-	W	(5), $I_L = 4.7\text{mA}$
Input Voltage	V_{BL}	21.6	24	26.4	V_{DC}	
Input Current	I_{BL}	-	3.8	-	A	Non Dimming
Input Ripple Noise	-	-	-	500	mV_{P-P}	$V_{BL} = 21.6\text{V}$
Backlight Turn on Voltage	V_{BS}	1790	-	-	V_{RMS}	$T_a = 0^\circ\text{C}$ (6)
		1200	-	-	V_{RMS}	$T_a = 25^\circ\text{C}$ (6)
Oscillating Frequency	F_W	54	56	58	kHz	
Dimming Frequency	F_B	150	160	170	Hz	
Minimum Duty Ratio	D_{MIN}	-	10	-	%	

Note (1) Lamp current is measured by utilizing high frequency current meters as shown below:



Note (2) The lamp starting voltage V_s should be applied to the lamp for more than 1 second under starting up duration. Otherwise the lamp could not be lighted on completed.

Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.

Note (4) The life time of a lamp is defined as when the brightness is larger than 50% of its original value and the effective discharge length is longer than 80% of its original length (Effective discharge length is defined as an area that has equal to or more than 70% brightness compared to the brightness at the center point.) as the time in which it continues to operate under the condition $T_a = 25 \pm 2^\circ\text{C}$ and $I_L = 4.4 \sim 5.0 \text{ mA}_{\text{RMS}}$.

Note (5) The power supply capacity should be higher than the total inverter power consumption P_{BL} . Since the pulse width modulation (PWM) mode was applied for backlight dimming, the driving current changed as PWM duty on and off. The transient response of power supply should be considered for the changing loading when inverter dimming.

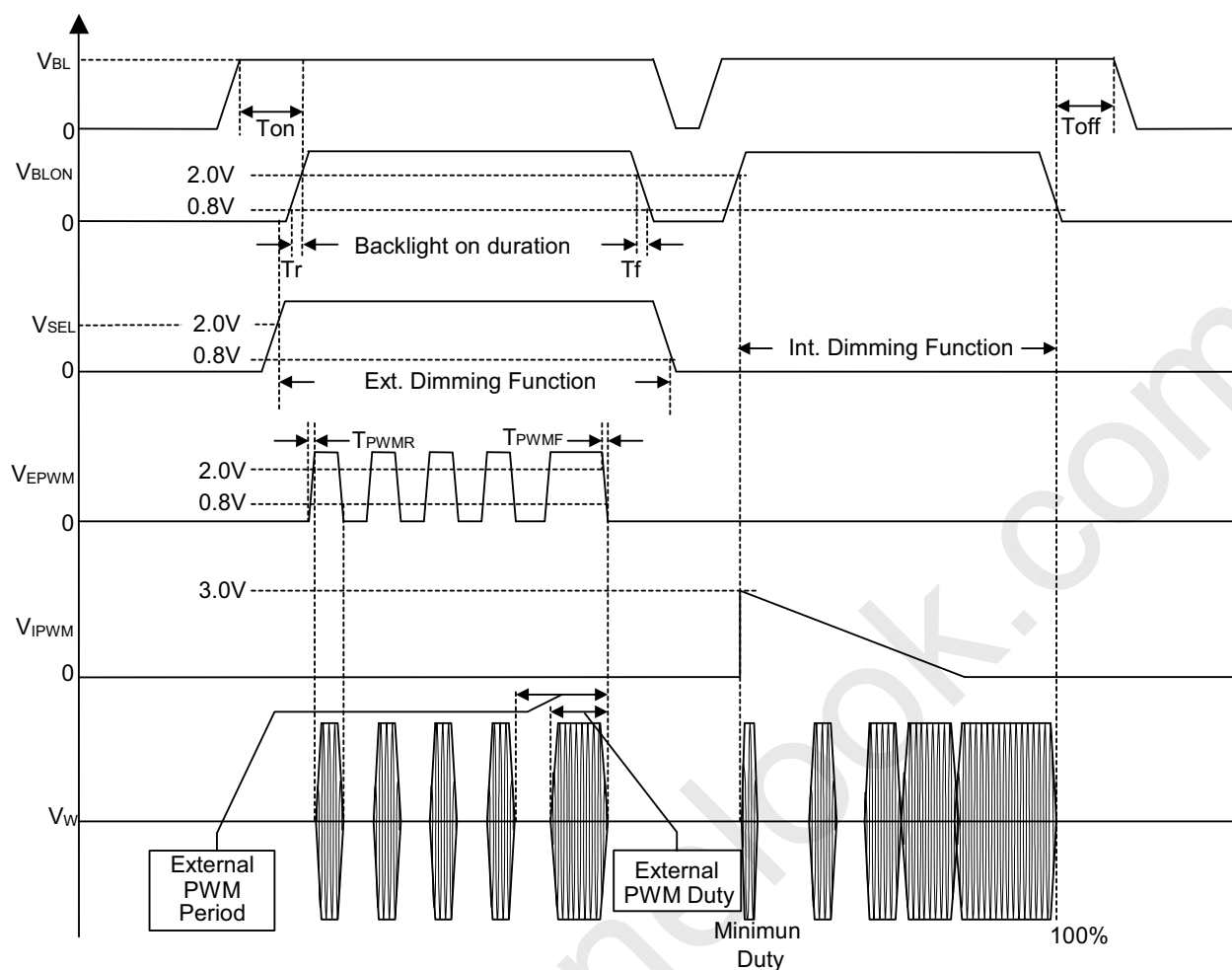
Note (6) The backlight turn on voltage should be under the rated withstanding voltage of transformer.

3.2.3 INVERTER INTERFACE CHARACTERISTICS

Item		Symbol	Test Condition	Min.	Typ.	Max.	Unit	Note
On/Off Control Voltage	ON	V_{BLON}	—	2.0	—	5.0	V	
	OFF		—	0	—	0.8	V	
Internal/External PWM Select Voltage	HI	V_{SEL}	—	2.0	—	5.0	V	
	LO		—	0	—	0.8	V	
Internal PWM Control Voltage	MAX	V_{IPWM}	$V_{\text{SEL}} = \text{L}$	—	—	3.0	V	minimum duty ratio
	MIN			—	0	—	V	maximum duty ratio
External PWM Control Voltage	HI	V_{EPWM}	$V_{\text{SEL}} = \text{H}$	2.0	—	5.0	V	duty on
	LO			0	—	0.8	V	duty off
Control Signal Rising Time		T_r	—	—	—	100	ms	
Control Signal Falling Time		T_f	—	—	—	100	ms	
PWM Signal Rising Time		T_{PWMR}	—	—	—	50	us	
PWM Signal Falling Time		T_{PWMF}	—	—	—	50	us	
Input impedance		R_{IN}	—	1	—	—	M Ω	
BLON Delay Time		T_{on}	—	300	—	500	ms	
BLON Off Time		T_{off}	—	300	—	500	ms	

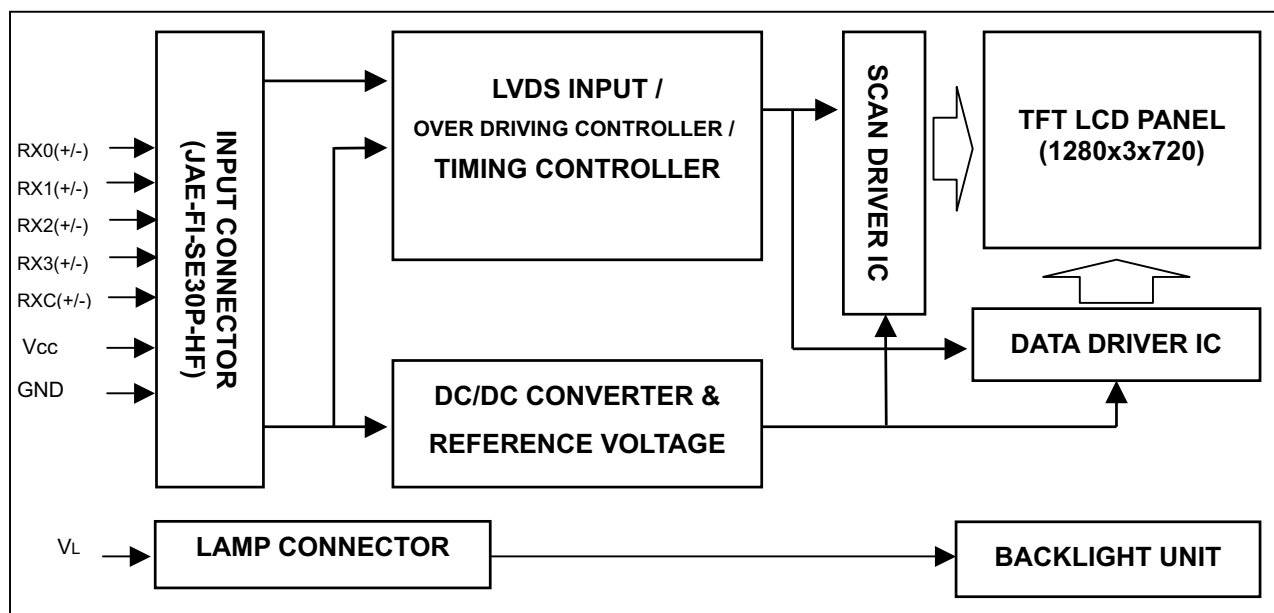
Note (1) The SEL signal should be valid before backlight turns on by BLON signal. It is inhibited to change the internal/external PWM selection (SEL) during backlight turn on period.

Note (2) The power sequence and control signal timing are shown as the following figure.



4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



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5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

Pin	Name	Description
1	NC	No Connection
2	NC	No Connection
3	NC	No Connection
4	NC	No Connection
5	NC	No Connection
6	NC	No Connection
7	NC	No Connection
8	GND	Ground
9	RX3+	Positive LVDS differential data input. Channel 3
10	RX3-	Negative LVDS differential data input. Channel 3
11	RXCLK+	Positive LVDS differential clock input.
12	RXCLK-	Negative LVDS differential clock input.
13	GND	Ground
14	GND	Ground
15	RX2+	Positive LVDS differential data input. Channel 2
16	RX2-	Negative LVDS differential data input. Channel 2
17	RX1+	Positive LVDS differential data input. Channel 1
18	RX1-	Negative LVDS differential data input. Channel 1
19	RX0+	Positive LVDS differential data input. Channel 0
20	RX0-	Negative LVDS differential data input. Channel 0
21	GND	Ground
22	GND	Ground
23	GND	Ground
24	GND	Ground
25	GND	Ground
26	VCC	+5.0V power supply
27	VCC	+5.0V power supply
28	VCC	+5.0V power supply
29	VCC	+5.0V power supply
30	VCC	+5.0V power supply

Note (1) Connector Part No.: FI-SE30P-HF (JAE)

Note (2) The first pixel is even.

5.2 BACKLIGHT UNIT

The pin configuration for the housing and leader wire is shown in the table below.

CN3-CN9 (Housing): BHR-03VS-1

Pin	Name	Description	Wire Color
1	HV	High Voltage	Pink
2	HV	High Voltage	White

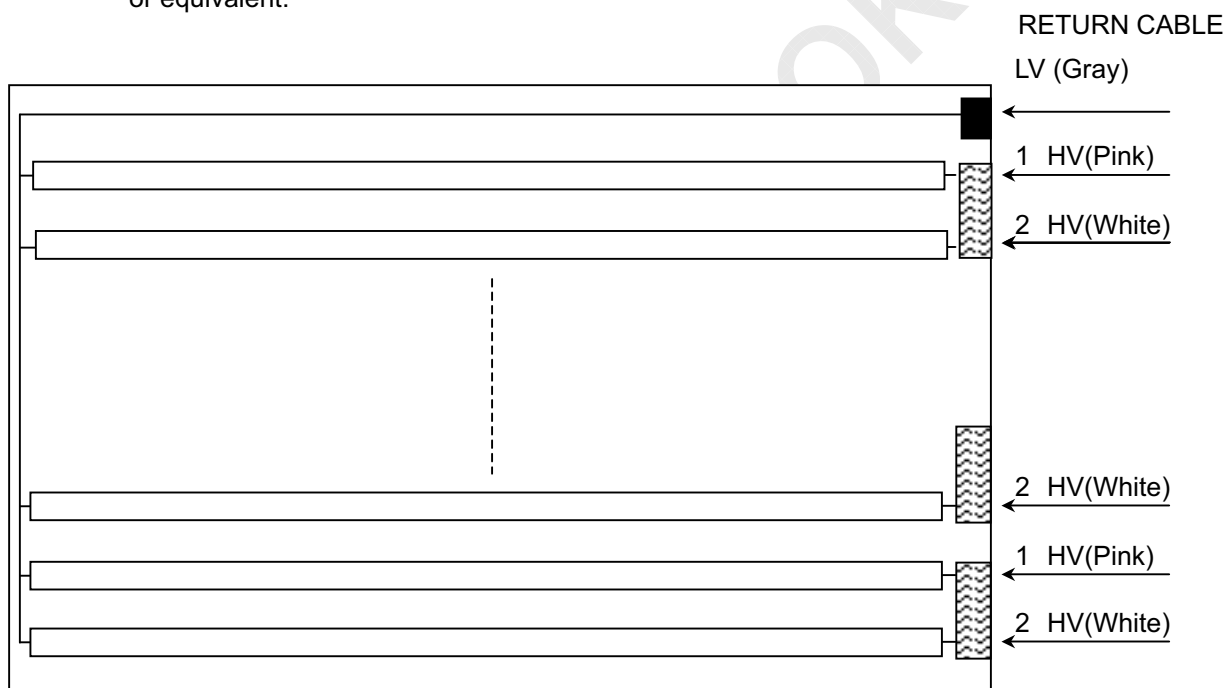
Note (1) The backlight interface housing for high voltage side is a model BHR-03VS-1, manufactured by JST.

The mating header on inverter part number is SM02(8.0)B-BHS-1-TB.

CN10 (Housing): ZHR-2 or equivalent

Pin	Name	Description	Wire Color
1	LV	Low Voltage	Gray
2	NC	-	-

Note (2) The backlight interface housing and return cable for low voltage side is a model ZHR-2 or equivalent, manufactured by JST or equivalent. The mating header on inverter part number is S2B-ZR-SM3A-TF or equivalent.



5.3 INVERTER UNIT

CN1(Header):S10B-PH-SM3-TB(JST) or equivalent.

Pin	Name	Description
1	VBL	+24V Power input
2		
3		
4		
5		
6	GND	Ground
7		
8		
9		
10		

CN2(Header): S12B-PH-SM3-TB(JST) or equivalent.

Pin	Name	Description
1	VBL	+24V Power input
2		
3		
4		
5		
6	GND	Ground
7		
8		
9	SEL	Internal/external PWM selection High : external dimming Low : internal dimming
10	E_PWM	External PWM control signal E_PWM should be connected to low when internal PWM was selected (SEL = low).
11	I_PWM	Internal PWM control signal I_PWM should be connected to ground when external PWM was selected (SEL = high).
12	BLON	Backlight on/off control

CN3-CN9(Header): SM02(8.0)B-BHS-1-TB(JST)

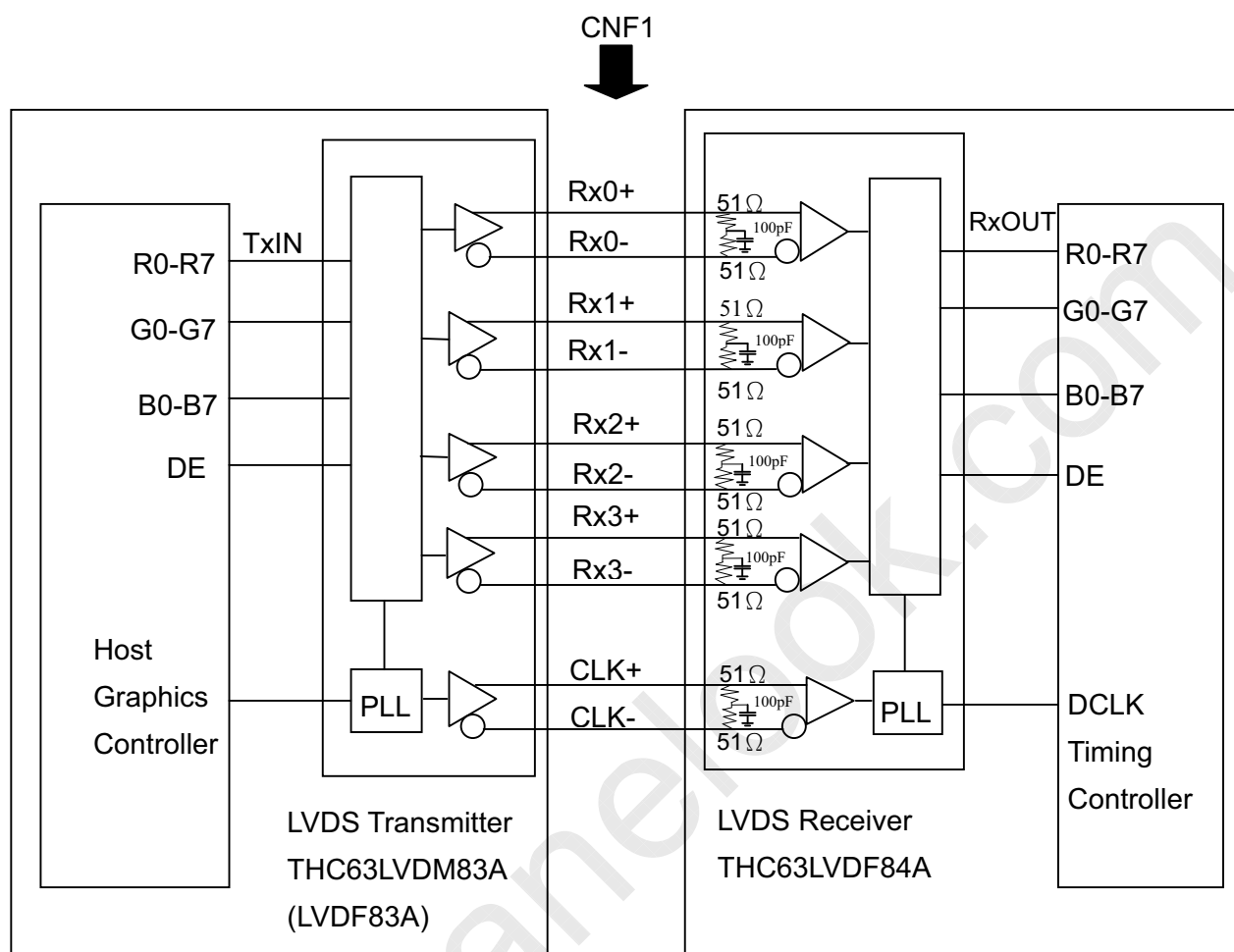
Pin	Name	Description
1	CCFL HOT	CCFL high voltage
2	CCFL HOT	CCFL high voltage

CN10(Header): S2B-ZR-SM3A-TF(JST) or equivalent

Pin	Name	Description
1	CCFL COLD	CCFL low voltage
2	NC	-

Note (1) Floating of any control signal is not allowed.

5.4 BLOCK DIAGRAM OF INTERFACE



R0~R7 : Pixel R Data
 G0~G7 : Pixel G Data
 B0~B7 : Pixel B Data
 DE : Display timing signal

- Notes:
- 1) The system must have the transmitter to drive the module.
 - 2) LVDS cable impedance shall be 50 ohms per signal line or about 100 ohms per twist-pair line when it is used differentially.

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	SIGNAL	TRANSMITTER THC63LVDM83A		INTERFACE CONNECTOR		RECEIVER THC63LVDF84A		TFT CONTROL INPUT
		PIN	INPUT	Host	TFT-LCD	PIN	OUTPUT	
24bit	R0	51	TxIN0	TA OUT0+	Rx 0+	27	Rx OUT0	R0
	R1	52	TxIN1			29	Rx OUT1	R1
	R2	54	TxIN2			30	Rx OUT2	R2
	R3	55	TxIN3			32	Rx OUT3	R3
	R4	56	TxIN4	TA OUT0-	Rx 0-	33	Rx OUT4	R4
	R5	3	TxIN6			35	Rx OUT6	R5
	G0	4	TxIN7			37	Rx OUT7	G0
	G1	6	TxIN8			38	Rx OUT8	G1
	G2	7	TxIN9	TA OUT1+	Rx 1+	39	Rx OUT9	G2
	G3	11	TxIN12			43	Rx OUT12	G3
	G4	12	TxIN13			45	Rx OUT13	G4
	G5	14	TxIN14			46	Rx OUT14	G5
	B0	15	TxIN15	TA OUT1-	Rx 1-	47	Rx OUT15	B0
	B1	19	TxIN18			51	Rx OUT18	B1
	B2	20	TxIN19			53	Rx OUT19	B2
	B3	22	TxIN20			54	Rx OUT20	B3
	B4	23	TxIN21	TA OUT2+	Rx 2+	55	Rx OUT21	B4
	B5	24	TxIN22			1	Rx OUT22	B5
	DE	30	TxIN26			6	Rx OUT26	DE
	R6	50	TxIN27			7	Rx OUT27	R6
	R7	2	TxIN5	TA OUT2-	Rx 2-	34	Rx OUT5	R7
	G6	8	TxIN10			41	Rx OUT10	G6
	G7	10	TxIN11			42	Rx OUT11	G7
	B6	16	TxIN16			49	Rx OUT16	B6
	B7	18	TxIN17	TA OUT3+	Rx 3+	50	Rx OUT17	B7
	RSVD 1	25	TxIN23			2	Rx OUT23	Not connect
	RSVD 2	27	TxIN24			3	Rx OUT24	Not connect
	RSVD 3	28	TxIN25			5	Rx OUT25	Not connect
	DCLK	31	TxCLK IN	TxCLK OUT+	RxCLK IN+	26	RxCLK OUT	DCLK
				TxCLK OUT-	RxCLK IN-			

R0~R7: Pixel R Data (7; MSB, 0; LSB)

G0~G7: Pixel G Data (7; MSB, 0; LSB)

B0~B7: Pixel B Data (7; MSB, 0; LSB)

DE : Display timing signal

Notes: 1)RSVD(reserved)pins on the transmitter shall be "H" or "L".

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The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	R7	R6	G5	G4	G3	G2	G1	G0	R7	R6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

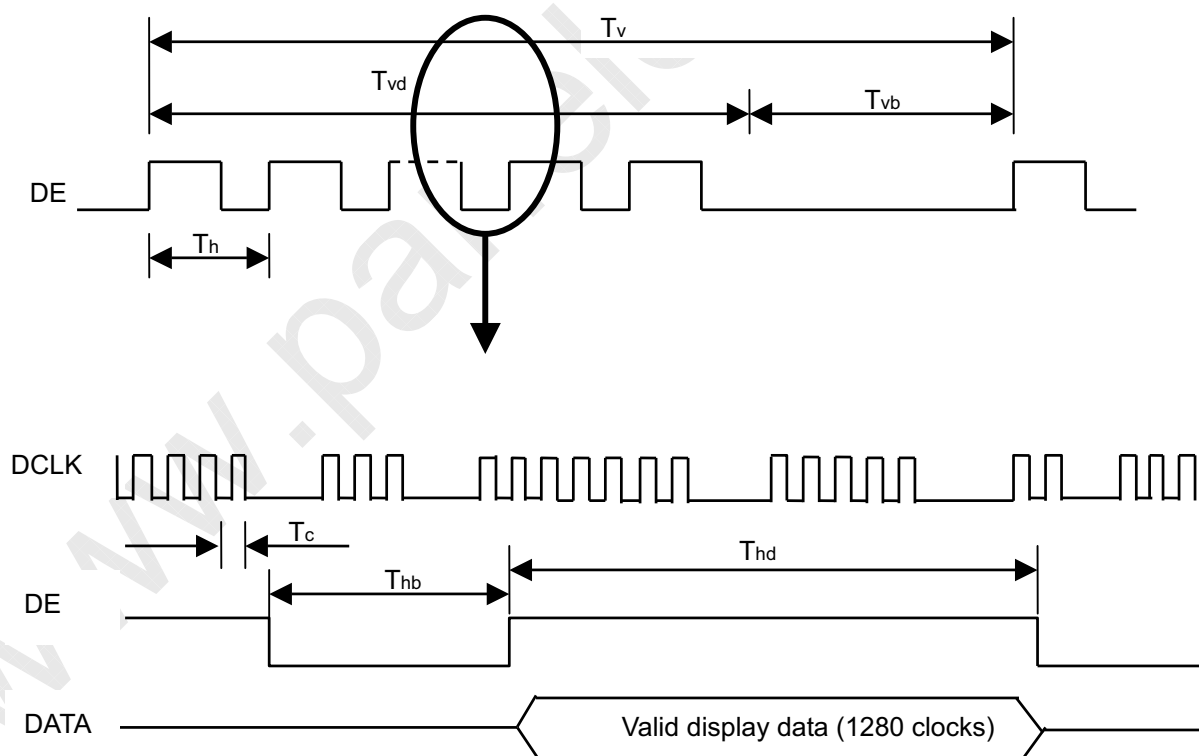
6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	1/Tc	70	74.25	80	MHZ	-
Vertical Active Display Term	Frame Rate	Fr	48	60	-	Hz	Tv=Tvd+Tvb
	Total	Tv	730	750	850	Th	-
	Display	Tvd	720	720	720	Th	-
	Blank	Tvb	10	30	130	Th	-
	Total	Th	1450	1650	2000	Tc	Th=Thd+Thb
Horizontal Active Display Term	Display	Thd	1280	1280	1280	Tc	-
	Blank	Thb	170	370	720	Tc	-

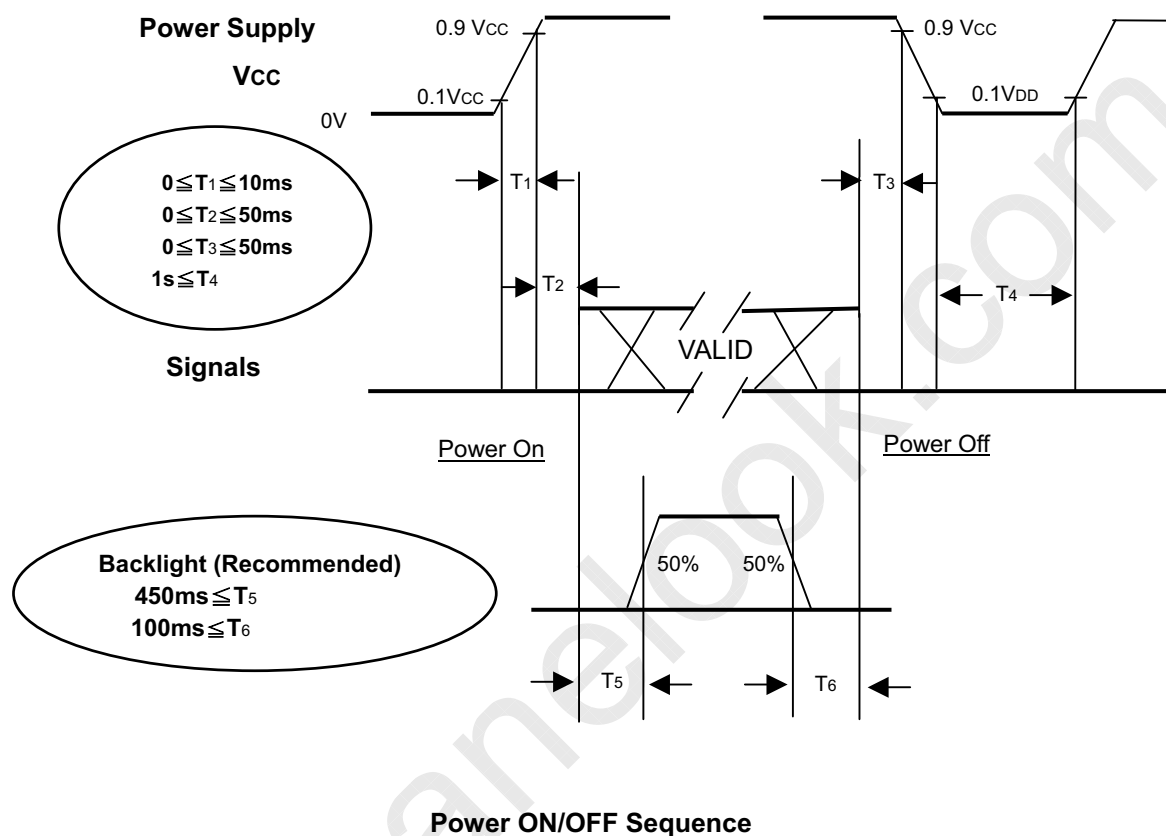
Note: Because of this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM



6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Note.

- (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2) Apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation of the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.
- (3) In case of vcc = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power of and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

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7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V _{CC}	5.0	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Lamp Current	I _L	4.7±0.3	mA
Oscillating Frequency (Inverter)	F _W	56±2	KHz

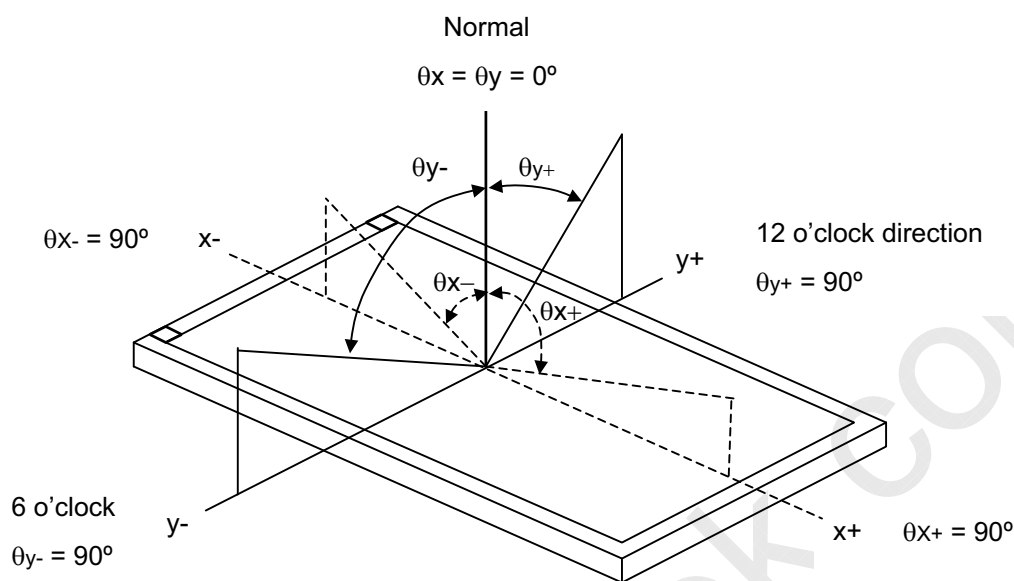
7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (7).

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing Normal Angle	400	600	-	-	Note(2)
Response Time		T _R		-	15	25	ms	Note(3)
		T _F		-	10	20	ms	
		Gray to gray			16.6		ms	Note(4)
Center Luminance of White		L _C		450	550	-	cd/m ²	Note(5)
Average Luminance of White		L _{AVE}		400	450	-	cd/m ²	
White Variation		ΔW		-	-	1.6	-	Note(8)
Cross Talk		CT		-	-	4.0	%	Note(6)
Color Chromaticity	Red	R _x		0.616	0.646	0.676	-	
		R _y		0.302	0.332	0.362	-	
	Green	G _x		0.239	0.269	0.299	-	
		G _y		0.570	0.600	0.630	-	
	Blue	B _x		0.112	0.142	0.172	-	
		B _y		0.042	0.072	0.102	-	
	White	W _x		0.255	0.285	0.315	-	9, 300K
		W _y		0.263	0.293	0.323	-	
Viewing Angle	Horizontal	θ _{x+}	CR≥10	80	85	-	Deg.	No gray scale inversion
		θ _{x-}		80	85	-		
	Vertical	θ _{y+}		80	85	-		
		θ _{y-}		80	85	-		

Note (1) Definition of Viewing Angle (θ_x , θ_y):

Viewing angles are measured by Eldim EZ-Contrast 160R



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{255} / L_0$$

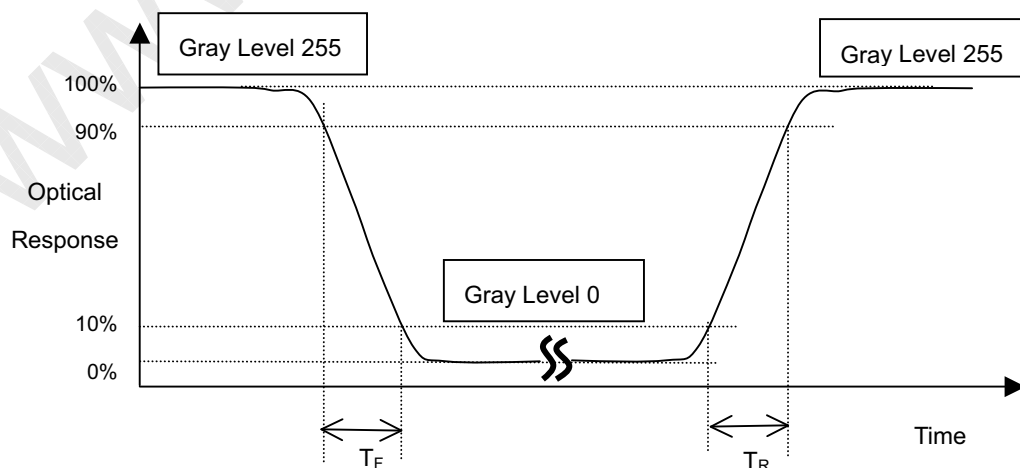
L255: Luminance of gray level 255

L 0: Luminance of gray level 0

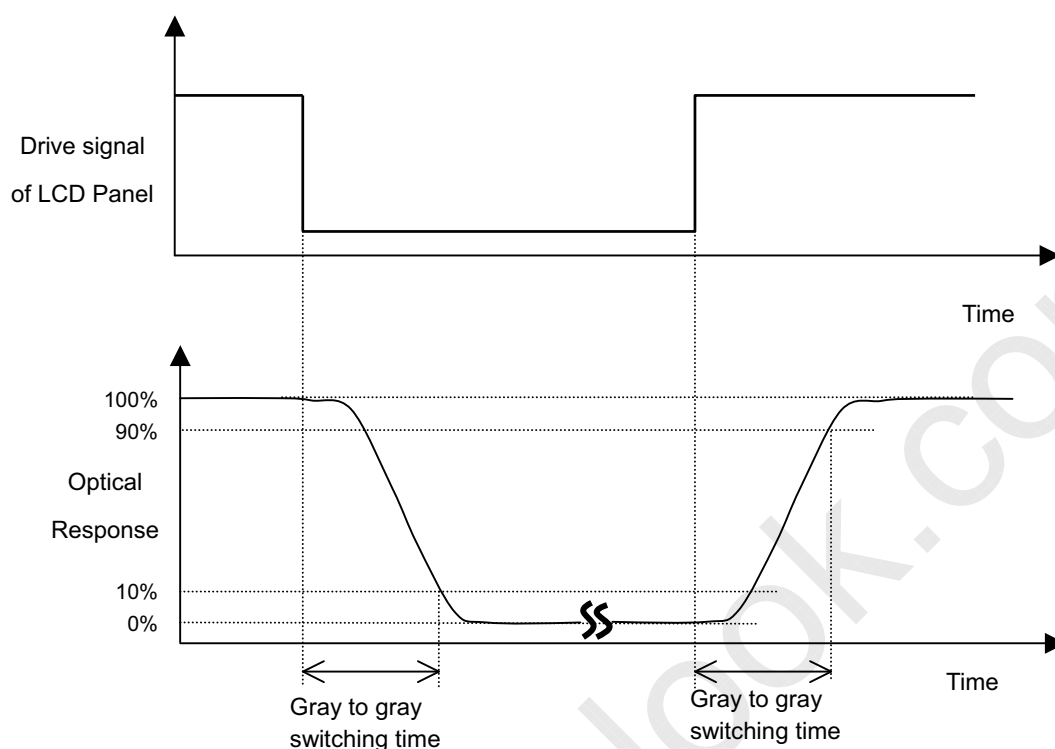
$$\text{CR} = \text{CR} (5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (8).

Note (3) Definition of Response Time (T_R , T_F):



Note (4) Definition of Gray to Gray Switching Time:



The driving signal means the signal of gray level 0,63,127,191,255.

Note (5) Definition of Luminance of White (L_C , L_{AVE}):

Measure the luminance of gray level 255 at center point and 5 points

$$L_C = L(5)$$

$$L_{AVE} = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

$L(x)$ is corresponding to the luminance of the point X at the figure in Note (8).

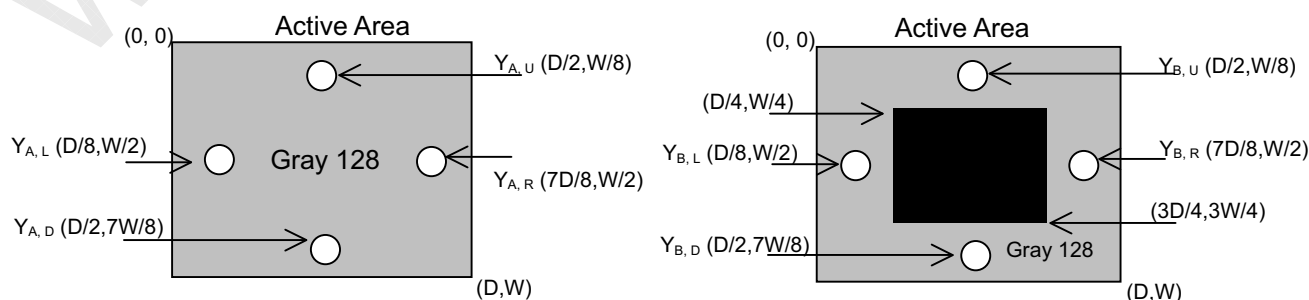
Note (6) Definition of Cross Talk (CT):

$$CT = |Y_B - Y_A| / Y_A \times 100 (\%)$$

Where:

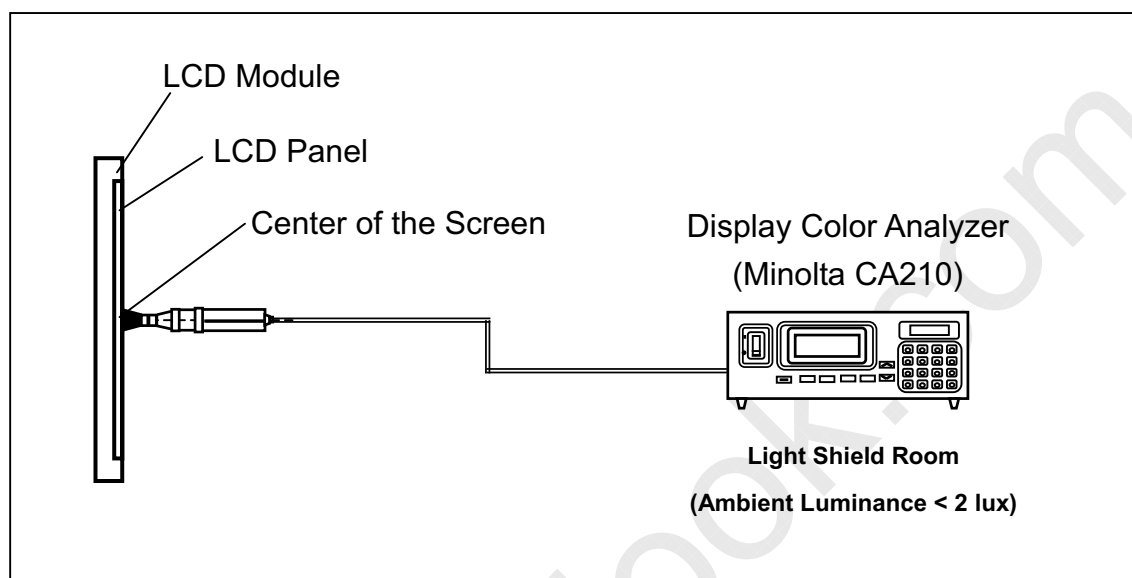
Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)



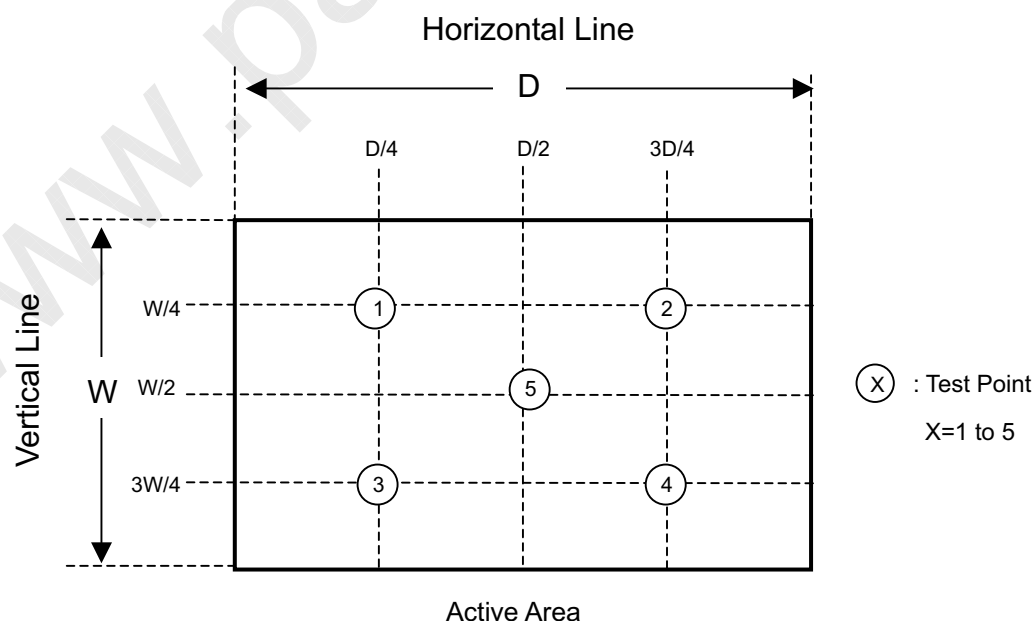
Note (7) Measurement Setup:

The LCD module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 1 hour in a windless room.

**Note (8) Definition of White Variation (δW):**

Measure the luminance of gray level 255 at 5 points

$$\delta W = \text{Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]}$$



8. PACKAGING

8.1 PACKING SPECIFICATIONS

- (1) 4 LCD TV Modules / Carton
- (2) Carton Dimensions : 742(L) X 327 (W) X 510 (H)
- (3) Weight : Approximately 19Kg (4 Modules Per Carton)

8.2 PACKING METHOD

Figures 8-1 and 8-2 are the packing method

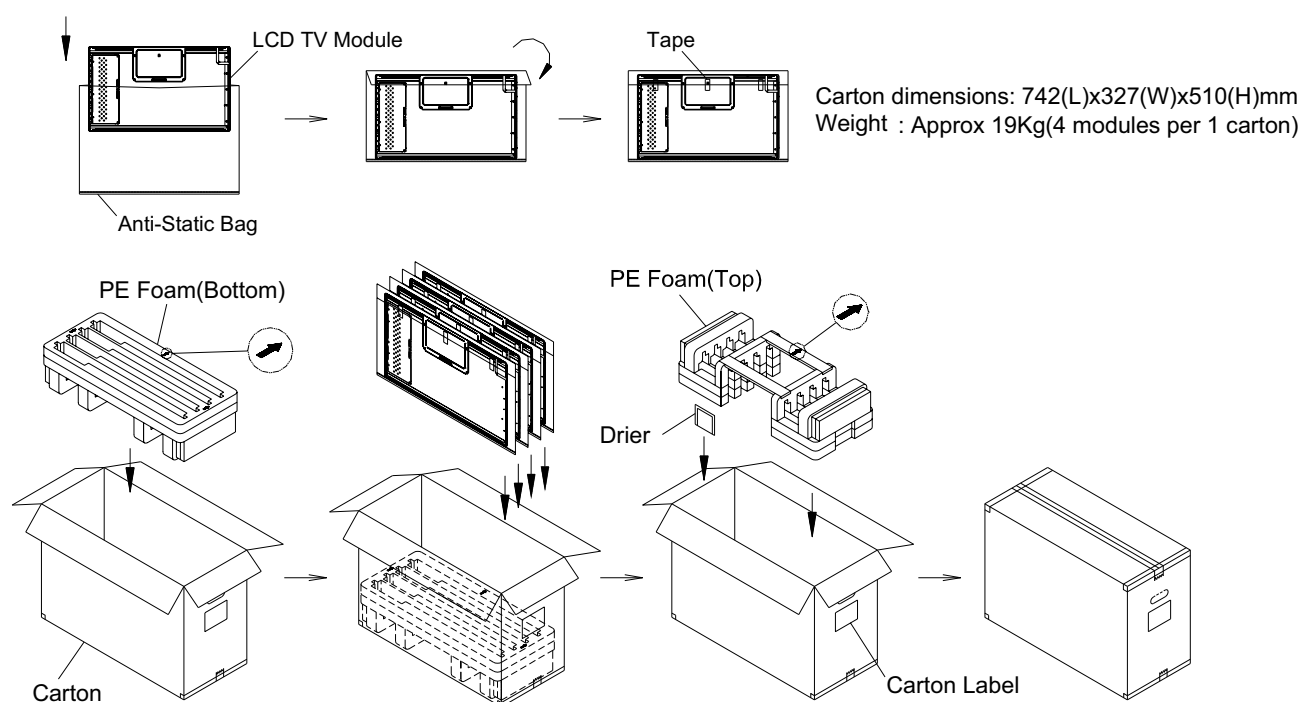


Figure.8-1 packing method

Corner Protector:L1020*50mm*50mm

Pallet:L1100*W1100*H135mm

Bottom Cap:L1100*W1100*H120mm

Pallet Stack:L1100*W1100*H1163mm

Gross Weight:180kg

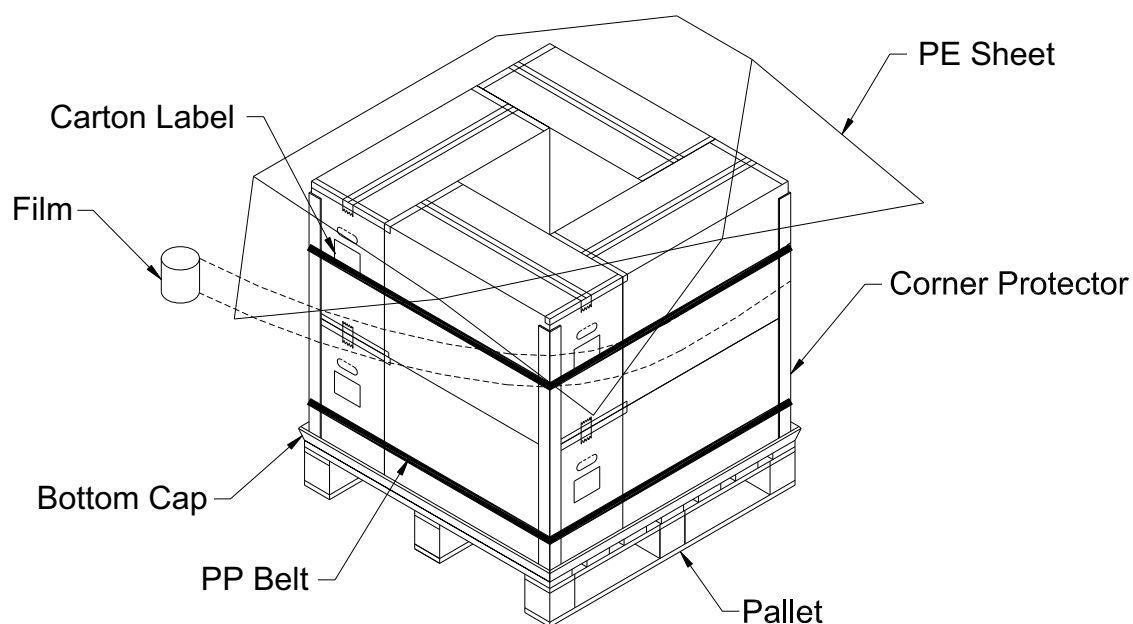


Figure. 8-2 packing method



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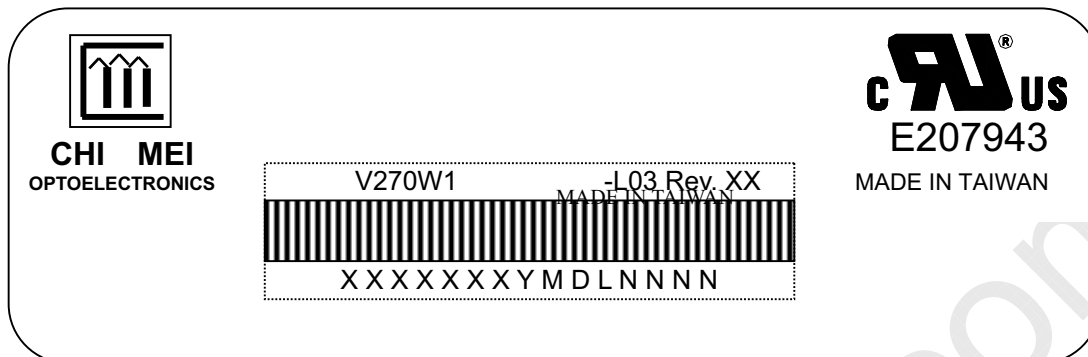
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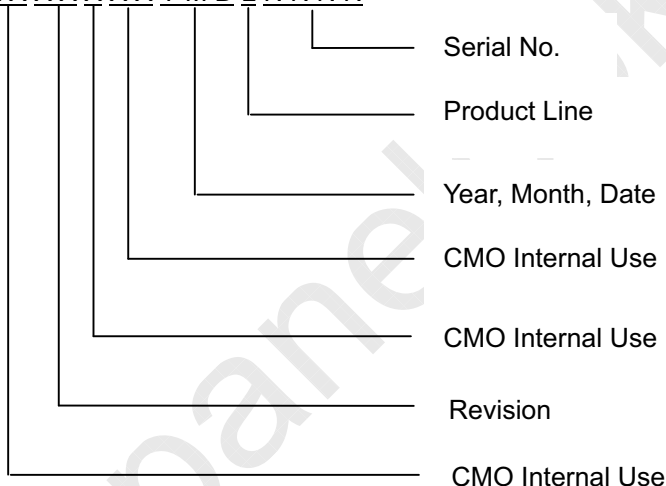
9. DEFINITION OF LABELS

9.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: V270W1-L03
 (b) Revision: Rev. XX, for example: A0, A1... B1, B2... or C1, C2...etc.
 (c) Serial ID: XXXXXXXXYMDLNNNN



Serial ID includes the information as below:

- (a) Manufactured Date: Year: 0~9, for 2000~2009
 Month: 1~9, A~C, for Jan. ~ Dec.
 Day: 1~9, A~Y, for 1st to 31st, exclude I, O, and U.
 (b) Revision Code: Cover all the change
 (c) Serial No.: Manufacturing sequence of product
 (d) Product Line: 1 -> Line1, 2 -> Line 2, ...etc.

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10. PRECAUTIONS

10.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) It is recommended to assemble or to install a module into the user's system in clean working areas.
The dust and oil may cause electrical short or worsen the polarizer.
- (3) Do not apply pressure or impulse to the module to prevent the damage of LCD panel and Backlight.
- (4) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.
- (5) Do not plug in or pull out the I/F connector while the module is in operation.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) Moisture can easily penetrate into LCD module and may cause the damage during operation.
- (9) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD modules in the specified storage conditions.
- (10) When ambient temperature is lower than 10°C, the display quality might be reduced. For example, the response time will become slow, and the starting voltage of CCFL will be higher than that of room temperature.

10.2 SAFETY PRECAUTIONS

- (1) The startup voltage of a Backlight is approximately 1000 Volts. It may cause an electrical shock while assembling with the inverter. Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

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